

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,401,211 B2  
DATED : July 15, 2008  
INVENTOR(S) : Sailesh KOTTAPALLI et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, Line 6 "reschedulimg" should be --rescheduling--

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PATENT NO. 7,401,211 B2

Date: July 14, 2009

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**Patent**

**Attorney Docket No.: 02207/10121**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Sailesh KOTTAPALLI et al.  
SERIAL NO. : 09/751,762  
FILED : December 29, 2000  
PATENT NO. : 7,401,211 B2  
ISSUED : July 15, 2008  
FOR : METHOD FOR CONVERTING PIPELINE STALLS CAUSED  
BY INSTRUCTIONS WITH LONG LATENCY MEMORY  
ACCESSES TO PIPELINE FLUSHES IN A MULTITHREADED  
PROCESSOR  
GROUP ART UNIT : 2183  
EXAMINER : Daniel PAN

M/S: CERTIFICATE OF CORRECTION BRANCH  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION**

Dear Sir:

We have compared the above patent with the application as filed and have found errors in the printing of the patent. We respectfully request that the enclosed Certificate of Correction on Form PTO-1050 be issued correcting the mistakes set forth therein under authority of 35 U.S.C. §254.

Application No.: 19/751,762  
Certificate of Correction dated: July 14, 2009  
Patent No.: 7,401,211 B2  
Issued on: July 15, 2008

The errors that appear in the patent are various typographical errors made by the Patent Office and no fee is believed required.

If any fee is required, the Office is hereby authorized to charge any additional fees, or credit any overpayments, to Kenyon & Kenyon LLP, Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON LLP

Dated: July 14, 2009

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